WHAT IS CLAIMED IS:

1. A time-driver circuit comprising:

resistive divider that includes a first resistor having a first end connected to a positive voltage supply and a second end connected to a resistive divider common node, and a second resistor having a first end connected to a negative voltage supply and a second end connected to the resistive divider common node;

a cascaded transistor structure that includes a first NMOS transistor having its drain connected to the positive supply voltage, its source connected to a common cascaded structure node and its gate connected to the resistive divider common node, and a second NMOS transistor having its drain connected to the common cascaded structure node, its source connected to the negative voltage supply and its gate connected to the negative voltage supply via a third resistor; and

a capacitor structure disposed between the common cascaded structure node and the positive voltage supply.

2. A time-divider circuit comprising:

a resistive divider that includes a first resistor having a first end connected to a positive voltage supply and a second end connected to a resistive divider common node, and a second resistor having a first end connected to a negative voltage supply and a second end connected to the resistive divider common node;

an NMOS transistor having its drain connected to the positive voltage supply, its source connected to the negative voltage supply and its gate connected to the resistive divider common node; and

a capacitor structure disposed between the source of the NMOS transistor and the positive supply voltage.